

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Noriaki Sakamoto et al.                      Art Unit : 2823  
Serial No. : 09/970,013                                      Examiner : Brook Kabede  
Filed : October 2, 2001  
Title : METHOD OF MANUFACTURING CIRCUIT DEVICE

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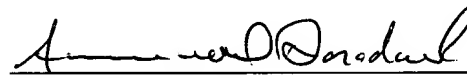
INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449. An English-language abstract is also included for this listed reference.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: 7/20/04



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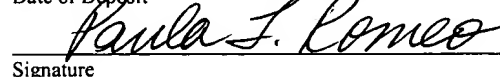
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10417-102001	Application No. 09/970,013
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))		Applicant Noriaki Sakamoto et al.	
		Filing Date October 2, 2001	Group Art Unit 2823

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
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	AF						
	AG						
	AH						
	AI						
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	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	WO/95/26047	9/28/1995	PCT				
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	